

FORM PTO-1449 U.S. DEPARTMENT OF COMMERCE  
PATENT AND TRADEMARK OFFICE

ATTY. DOCKET NO.  
DP-706 US

SERIAL NO.

**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

(Use several sheets if necessary)

(37 CFR 1.98(b))

APPLICANT  
Masaru TSUKIJI

FILING DATE  
January 18, 2001

GROUP

**U.S. PATENT DOCUMENTS**

EXAMINER INITIAL		PATENT NUMBER	ISSUE DATE	PATENTEE	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
	AA						
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						

**FOREIGN PATENT OR PUBLISHED FOREIGN PATENT APPLICATION**

		DOCUMENT NO.	PUBL. DATE	COUNTRY OR PATENT OFFICE	CLASS	SUB CLASS	TRANSLATION YES NO
	AL						
	AM						
	AN						
	AO						
	AP						

**OTHER DOCUMENTS (Including Author, Title, Date, Relevant Pages, Place of Publication)**

FR	AR	M. KATO et al., "A Shallow-Trench-Isolation Flash Memory Technology with a Source-Bias Programming Method", 1996, <u>IEEE TECHNICAL DIGEST OF IEDM</u> , pages 177-180.
	AS	

EXAMINER

DATE CONSIDERED

EXAMINER: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.